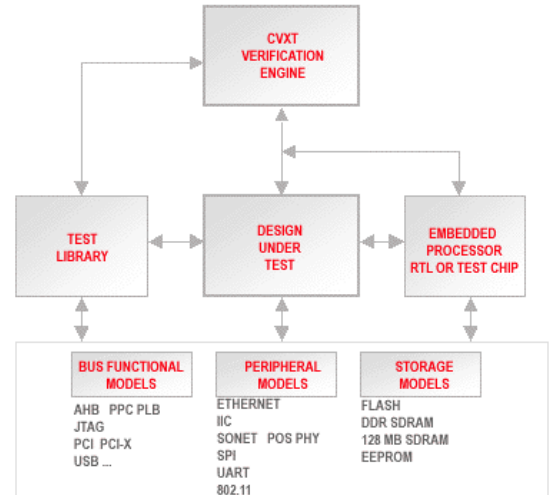


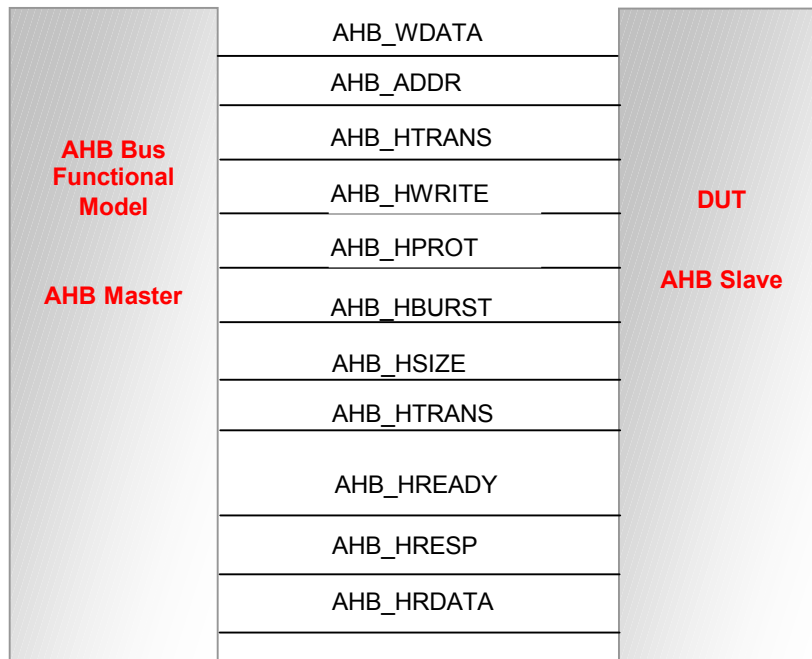
Fiesta[®] AHB Bus Functional Model

Features

- Interfaces with Comit Fiesta[®] CVXT Open Verification Environment
- Configurable to test different types of AHB slaves and all possible AHB burst sizes
- Internally generated clock signal with parameterized frequency
- Descriptors to set the following:
 - AHB burst type (SINGLE, INCR, INCR4, WRAP4, INCR8, WRAP8, INCR16, WRAP16).
 - Increment size in case of INCR burst.
 - Hsize (BYTE, HALFWORD, WORD).
 - Hprot (Cacheable, Bufferable, Privilege, Data/opcode)
 - Start Address for a burst.
 - Start data for Write access.
 - Busy cycle insertion within a burst.
 - Idle cycle insertion between bursts.
 - Burst terminate and Locked transfer
- 32 bit wide memory



System Interface Diagram



Fiesta[®] Process Standardization and Acceleration Tool Kit is an industrial strength suite of tools designed, developed, tested and used by engineers of Comit's Contract Engineering Center. Their experience in developing processes and methodology that yield predictable and accurate results forms the foundation of the toolkit. Use it with confidence.

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